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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/862,484	05/23/2001	Feng-Ting Pai	0941-0261P-SP	8958

2292 7590 12/19/2003

BIRCH STEWART KOLASCH & BIRCH  
PO BOX 747  
FALLS CHURCH, VA 22040-0747

EXAMINER
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SHAPIRO, LEONID

ART UNIT	PAPER NUMBER
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2673

DATE MAILED: 12/19/2003

9

Please find below and/or attached an Office communication concerning this application or proceeding.

TS

**Advisory Action**

Application No.

09/862,484

Applicant(s)

PAI ET AL.

Examiner

Leonid Shapiro

Art Unit

2673

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 3 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY** [check either a) or b)]

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.
- ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☐ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
  - (b) ☐ they raise the issue of new matter (see Note below);
  - (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_

3. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
4. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☒ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_.

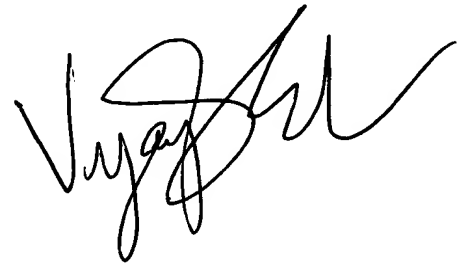
Claim(s) objected to: \_\_\_\_\_.

Claim(s) rejected: 1, 2, 4 and 6-8.

Claim(s) withdrawn from consideration: \_\_\_\_\_.

8. ☐ The drawing correction filed on \_\_\_\_\_ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). \_\_\_\_\_.
10. ☐ Other: \_\_\_\_\_

Continuation of 5. does NOT place the application in condition for allowance because: As to claims 1 and 6, APA teaches a method of processing signals of a timing controller of a liquid crystal display module, comprising the steps of: receiving a vertical synchronizing signal (See Fig. 3-4, items VSYNC, in description See from page 1, Line 27 to page 2, Line 6); receiving a data enable signal DE which has a vertical blank period, decoding the signal to generate a vertical synchronizing signal (See Fig. 3-4, items DE, VB, in description See from page 1, Line 27 to page 2, Line 6); generating a gate clock signal CPV which has a plurality gate clock cycles C1-Cn (See Fig. 3-6, item CPV, in description See page 2, Lines 4-24); after a rising edge or a falling edge of the vertical synchronizing signal generating a plurality of gate-on enable signals OE simultaneously according to the plurality of gate clock cycles C1-Cn of gate clock CPV (See Fig. 3-6, item OE, in description See page 2, Lines 4-24); after a rising edge or a falling edge of the vertical synchronizing signal generating start vertical signals STV before the end of the vertical blank period VB after at least a gate clock cycle C1 during vertical blank period VB (See Fig. 3-6, items STV1, STV2, in description See page 2, Lines 4-6) and APA teaches a first start vertical signal STV1, for determining a start scan location of a frame; and a second start vertical signal STV2, for offsetting flicker and display brightness of the liquid crystal display (See Fig. 3-6, items STV1, STV2, in description See page 2, Lines 4-6).. .



**VIJAY SHANKAR**  
**PRIMARY EXAMINER**